

1/7

PCT/US98/14195

2/7

22 TO TIU TO SIU 53 CELL ASSEMBLER Byte Inter-Leaver RSENCODER RSDECODER 52 FIG. 3 FRAME ASSEMBLER byte Deinter-Leaver 21 CELL PRE-PROCESSOR ACQ. SYNC UNIT 8 . ක **FROM TIU FROM SIU**

FIG. 2

FIG. 4

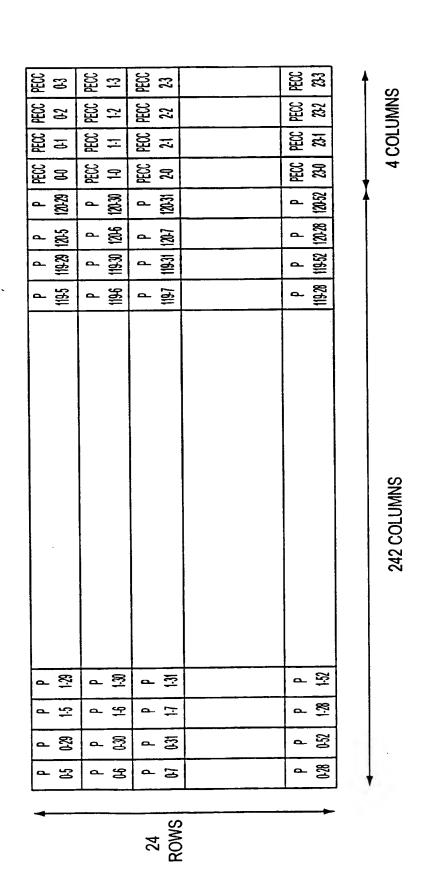
			0-3 HEUU-Z		0-2 NECC-2		0-1 HECC-2		0-0 HECC-2	HECC-2
MNS		-25	HECC-1 6	- CC-1	HECC-1 60-2		HECC-1 60-1	;	HECC-1 60-0	HECC-1
126 COLUMNS		工	61-3	工	61-2	H	61-1	I	61-0	
*		H H H H	19-3 118-3 117-3 116-3	I I I	19-2 118-2 117-2 116-2	I I I	120-1 119-1 118-1 117-1 116-1	HHHH	120-0 119-0 118-0 117-0 116-0	
		H	1 120-3119-3118-31	I O	2 1120-2111	E O	3 120-111	I	120-011	
5 ROWS									_	

SUBSTITUTE SHEET (RULE 26)

PCT/US98/14195

FIG. 5

EXTRA FEC - UP TO 8 CELLS/16 COLUMNS



SUBSTITUTE SHEET (RULE 26)

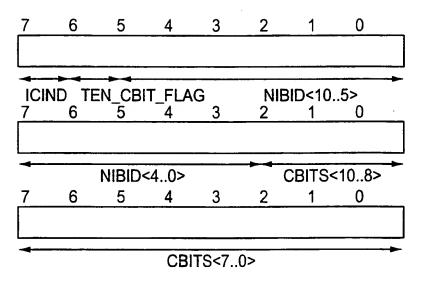


FIG. 6

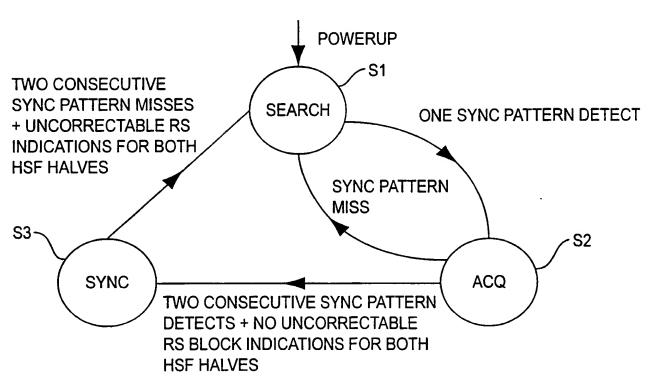
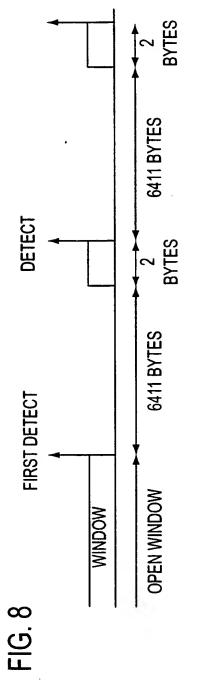


FIG. 7



NO ALARMS FERF FERF FERF SAT. RECEIVE **IDLE/FERF** DLE AIS/FERF AIS AIS AIS AIS AIS AIS/FERF SOI AIS AIS AIS AIS NO ALARMS FERF SOT AIS INPUTS TERR. TRANS-

FIG. 9



FIG. 10

				(0	(0)	[(0			
	AIS	AIS	IDLE	NO ALARMS	NO ALARMS	NO ALARMS			
	AIS	AIS	IDLE	NO ALARMS	NO ALARMS	FERF	ш		
	AIS	AIS	IDLE	NO ALARMS	NO ALARMS	DLE	SAT. RECEIVE		
	AIS	AIS	IDLE	NO ALARMS NO ALARMS NO ALARMS	NO ALARMS NO ALARMS NO ALARMS NO ALARMS	AIS			
	AIS/FERF	AIS/FERF	IDLE/FERF	FERF	FERF	SOT			
	SOT	AIS	IDLE	FERF	NO ALARMS	INPUTS			
,		A N							